

Notice of References Cited

Application/Control No.	Applicant(s)/Pate	nt Under
09/955,558	Reexamination GREENBERG ET	ΓAL.
Examiner	Art Unit	
Stephen M. Baker	2133	Page 1 of 1

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NON-PATENT DOCUMENTS

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	V	Biver et al., "Architectural Design and Realization of a Single-Chip Viterbi Decoder", INTEGRATION, the VLSI Journal 8 (1989), pp. 3-16.	
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	x	Choi et al., "Viterbi Detector Architecture for High-Speed Optical Storage", 1997 IEEE TENCON, Vol. 1, Dec. 1997, pp. 89-92.	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.